
Timeflux BrainFlow

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This plugin provides a unified interface for all boards supported by [BrainFlow](#).

INSTALLATION

First, make sure that `Timeflux` is installed.

You can then install this plugin in the `timeflux` environment:

```
$ conda activate timeflux
$ pip install timeflux_brainflow
```

1.1 API Reference

This page contains auto-generated API reference documentation.

`timeflux_brainflow`

1.1.1 timeflux_brainflow

`timeflux_brainflow.nodes`

nodes

`timeflux_brainflow.nodes.driver`

driver

class `timeflux_brainflow.nodes.driver.BrainFlow` (`board`, `channels=None`, `command=None`, `debug=False`, `**kwargs`)

Bases: `timeflux.core.node.Node`

Driver for BrainFlow.

This plugin provides a unified interface for all boards supported by BrainFlow.

Variables `o (Port)` – Default output, provides DataFrame.

Parameters

- **board** (*string/int*) – The board ID. Allowed values: numeric ID or name (e.g. `synthetic`, `cyton_wifi`, `brainbit`, etc.).
- **channels** (*list*) – The EEG channel labels. If not set, incrementing numbers will be used.
- **command** (*string*) – Send a command to the board. Use it carefully and only if you understand what you are doing.
- **debug** (*boolean*) – Print debug messages.
- ****kwargs** – The parameters specific for each board. Allowed arguments: `serial_port`, `mac_address`, `ip_address`, `ip_port`, `ip_protocol`, `serial_number`, `other_info`.

See also:

List of [supported boards](#).

Example

```
graphs:
- id: Synthetic
  nodes:
  - id: acquire
    module: timeflux_brainflow.nodes.driver
    class: BrainFlow
    params:
      board: synthetic
  - id: display
    module: timeflux.nodes.debug
    class: Display
  edges:
  - source: acquire
    target: display
  rate: 10
```

Instantiate the node.

update (*self*)

Update the input and output ports.

terminate (*self*)

Perform cleanup upon termination.

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